

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Peter Kochersperger

Appl. No.: 10/780,877

Filed: February 19, 2004

For: **Overlay Correction By Reducing  
Wafer Slipping After Alignment**

Confirmation No.: 1990

Art Unit: 2822

Examiner: Duong, Khanh B.

Atty. Docket: 1857.2140000

**Notice of Appeal From the Examiner to the Board  
of Patent Appeals and Interferences – Large Entity**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the final decision of the Examiner dated November 2, 2007, in which claims 12-14 and 23 were finally or twice rejected.

The fee (for a large entity) for filing a Notice of Appeal from the Examiner to the Board of Patent Appeals and Interferences (37 C.F.R. § 41.20(b)(1)), along with any necessary extension fees (37 C.F.R. § 1.17(a)), is provided in the attached PTO-2038 Credit Card Payment Form. In the event that extensions of time under 37 C.F.R. § 1.136 other than those otherwise provided for herewith are required to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Date: 2/4/08

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